Notice of Allowability	Application No.	Applicant(s)	
	10/815,058	WU, STEPHEN	
	Examiner	Art Unit	
	Sanh D. Phu	2618	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to *the Amendment filed on 7/11/2007*.

2. ☒ The allowed claim(s) is/are *1-21*.

3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
   a) ☐ All b) ☐ Some* c) ☐ None of the:
      1. ☐ Certified copies of the priority documents have been received.
      2. ☐ Certified copies of the priority documents have been received in Application No. _____ .
      3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

   * Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.

5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
   (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
      1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
   (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

   Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).

6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application
6. ☐ Interview Summary (PTO-413), Paper No./Mail Date _____ .
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____.

DETAILED ACTION

1. This Office Action is responsive to the Amendment filed on 7/11/07. Accordingly, claims 1-21 are currently pending.

REASONS FOR ALLOWANCE

2. Claims 1-21 are allowed.

3. The following is an examiner's statement of reasons for allowance:

-Regarding independent claim 1, none of prior art of record teaches or suggests a method for forming an integrated circuit radio frequency transceiver with reduced harmonic interference from undesired coupling, comprising procedures of forming a plurality of signal traces on a top metal layer, the plurality of signal traces being operably disposed to carry ingoing signals on a receive path or outgoing signals on a transmit path of the integrated circuit radio frequency transceiver; and forming a resistive block in line with at least one signal trace of the plurality of signal traces wherein the resistive block comprises an input and an output and at least one resistive element and at least one trace, wherein the at least one resistive element and the at least one trace of the resistive block are operably disposed in parallel between the input

and output of the resistive block and wherein the at least one trace of the resistive block is operable to short the at least one resistive element; evaluating real performance of the sample of the integrated circuit radio frequency transceiver; determining whether harmonic interference may need to be reduced; and selecting a first trace of the at least one trace and removing a short across at least one shorted resistive element of the resistive block operably disposed in line with the first trace to form an unshorted resistive block.

-Regarding independent claim 9, none of prior art of record teaches or suggests a method for forming an integrated circuit radio frequency transceiver with reduced harmonic interference from undesired coupling, comprising: forming a plurality of traces on a top metal layer, the plurality of traces being disposed to carry receive path or transmit path signals of the integrated circuit radio frequency transceiver; and forming a resistive block in line with at least one signal trace of the plurality of signal traces wherein the resistive block comprises an input and an output and at least one resistive element and at least one trace, wherein the at least one resistive element and the at least one

trace of the resistive block are operably disposed in parallel between the input and output of the resistive block and wherein the at least one trace of the resistive block is operable to short the at least one resistive element; evaluating real performance of the sample of the integrated circuit radio frequency transceiver; determining whether harmonic interference may need to be reduced; selecting and removing a first short across a first resistor operably disposed in line with the first trace of the plurality of series coupled resistors to create an RC filter resulting from a coupling of parasitic capacitance of the trace and an unshorted first resistor.

-Regarding independent claim 15, none of prior art of record teaches or suggests a method for forming an integrated circuit radio frequency transceiver with reduced harmonic from undesired coupling, comprising: forming a plurality of traces on a top metal layer, the plurality of traces being operably disposed to carry receive path or transmit path signals of the integrated circuit radio frequency transceiver; and forming a resistive block in line with at least one signal trace of the plurality of signal traces wherein the resistive block

comprises an input and an output and at least one resistive element and at least one trace, wherein the at least one resistive element and the at least one trace of the resistive block are operably disposed in parallel between the input and output of the resistive block and wherein the at least one trace of the resistive block is operable to short the at least one resistive element; evaluating real performance of the sample of the integrated circuit radio frequency transceiver; determining whether harmonic interference may need to be reduced; and selecting and removing the short across the parallel coupled resistors of the resistive block to create an RC filter resulting from a coupling of parasitic capacitance of the trace and unshorted parallel coupled resistors.

-Regarding independent claim 20, none of prior art of record teaches or suggests an integrated circuit radio transceiver, comprising: front end transceiver circuitry wherein the front end transceiver circuitry includes a plurality of traces on a top metal layer, the plurality of traces being operably disposed to carry receive path or transmit path signals of the integrated circuit radio frequency transceiver; and a resistive block in line with at least one trace

of the plurality of traces wherein the resistive block comprises an input and an output and at least one resistive element and at least one trace, wherein the at leasl one resistive element and the at least one trace of the resistive block are operably disposed in parallel between the input and output of the resistive block and wherein the at least one trace of the resistive block is operable to short the at least one resistive element.

-Regarding independent claim 21, none of prior art of record teaches or suggests an integrated circuit radio transceiver, comprising: front end transceiver circuitry; wherein the front end transceiver circuitry includes a plurality of traces on a top metal layer; and a shorted resistive block in line with at least one trace of the plurality of traces wherein the resistive block comprises an input and an output and at least one resistive element and at least one trace, wherein the at least one resistive element and the at least one trace of the resistive block are operably disposed in parallel between the input and output of the resistive block and wherein the at least one trace of the resistive block is operable to short the at least one resistive element.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Sanh D. Phu whose telephone number is (571)272-7857. The examiner can normally be reached on M-Fr from 8:00-16:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew D. Anderson can be reached on (571) 272-4177. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Sanh D. Phu
Patent Examiner
Division 2618

7/21/07

SP

SANH D. PHU
PATENT EXAMINER